

METHOD OF CONTROLLING AN INTEGRATED CIRCUIT
CAPABLE OF SIMULTANEOUSLY PERFORMING
A DATA READ OPERATION AND A DATA WRITE OPERATION

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ABSTRACT OF THE DISCLOSURE

A method of controlling an integrated circuit (IC) capable of simultaneously performing a data read operation and a data write operation is provided. The method comprises (a) receiving a write address, a read address, and write data,
10 (b) determining, a memory block and a data memory block in which a data read operation and a data write operation are to be performed in response to the write address and the read address, (c) performing the data read operation or the data write operation in the data memory block according to the determination of step (b), and (d) performing the data read operation or the data write operation in the
15 memory block according to the determination of step (b).